

1 High-Level Synthesis for FPGA, Part 3: Advanced

AXIInHLS-AXI Addressing Vivado: Quiz Solution

www.highlevel-synthesis.com

There are two slaves: /axi_gpio_0/S_AXI and /axi_gpio_1/S_AXI

/axi_gpio_0/S_AXI

Address offset: 0x0

Address range: 64K

/axi_gpio_1/S_AXI

Address offset: 0x10000

Address range: 64K