

1 High-Level Synthesis for FPGA, Part 3: Advanced

These are the synthesis error messages.

ERROR: [HLS 207-3696] assigning to 'ap_uint<12> *' from incompatible type 'ap_uint<8> [10]' (letter_number-vitishls/letter_number.cpp:46:9)

ERROR: [HLS 207-3696] assigning to 'ap_uint<12> *' from incompatible type 'ap_uint<8> [10]' (letter_number-vitishls/letter_number.cpp:48:9)

