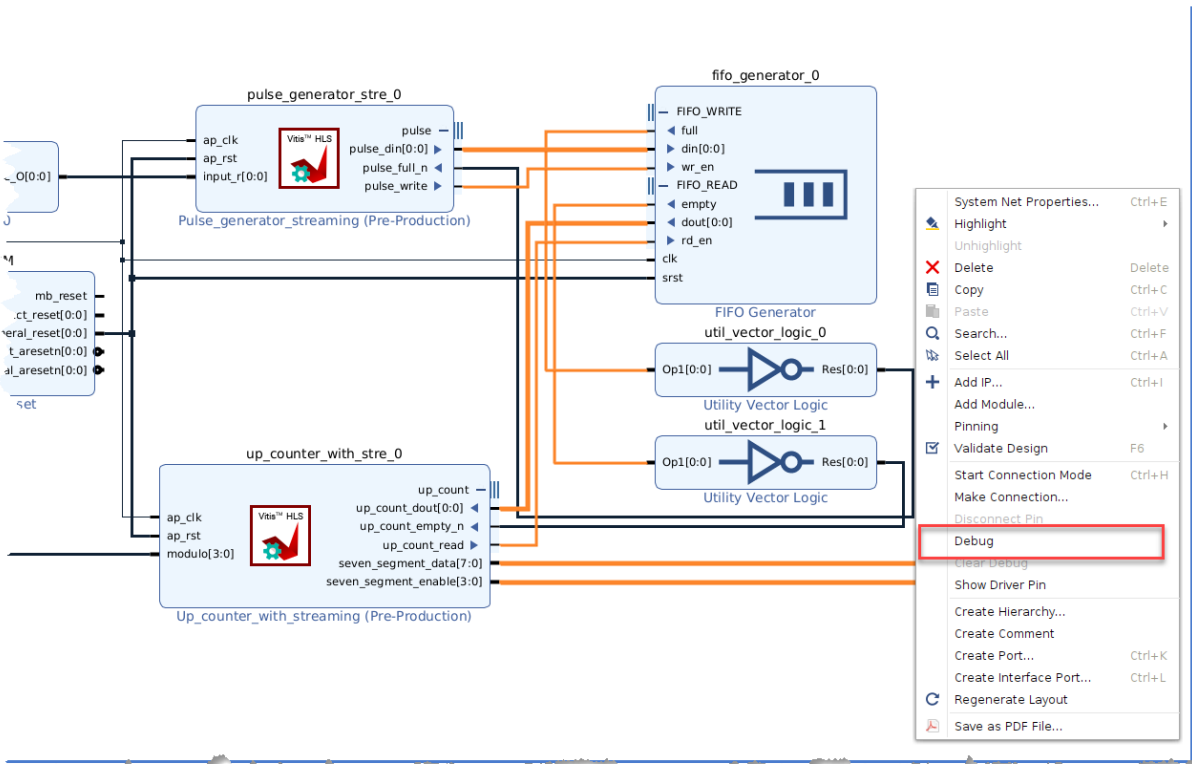
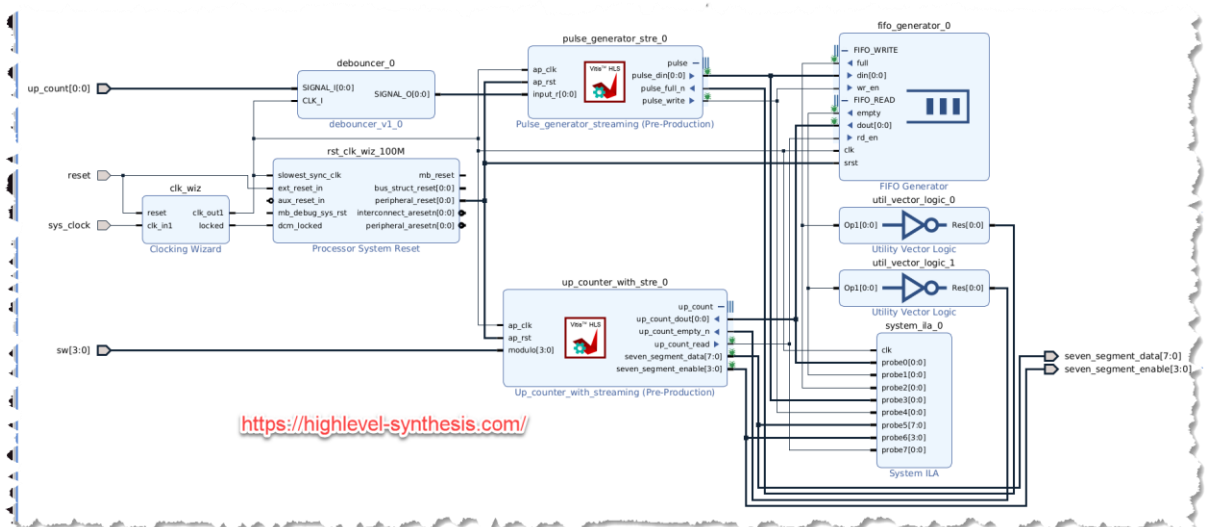


1 High-Level Synthesis for FPGA, Part 3: Advanced

- 1- Select a few signals of the design.
- 2- Right-click on a selected signal and choose the Debug Option



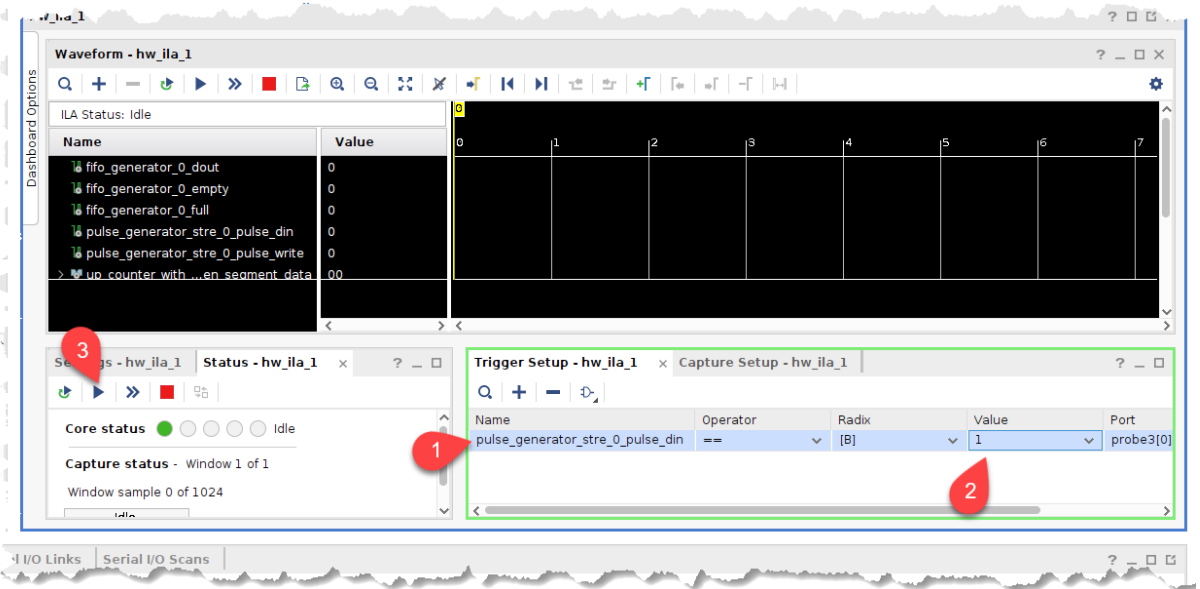
- 3- Then click on the “Run Connection Automation” link and press OK after selecting all the options on the left.



- 4- Generate the bitstream and program the board

2 High-Level Synthesis for FPGA, Part 3: Advanced

- 5- Add the pulse_generator_stre_0_pulse_din as the trigger signal and set the value to 1. Then click on the run icon in the Status view. The debugger is waiting for you to press the UP push button.



- 6- Press the push-button and check the waveforms

