This figure shows the C-simulation output. As can be seen, each function call generates a new data bit on the output.

```
INFO: [SIM 2] *************** CSIM start ************
INFO: [SIM 4] CSIM will launch GCC as the compiler.
   Compiling ../../../parallel2serial-tb.cpp in debug
   Compiling ../../../parallel2serial.cpp in debug mo
   Generating csim.exe
d = 1 serial_start = 1 serial_end = 0
d = 1 serial_start = 0 serial_end = 0
d = 0 serial_start = 0 serial_end = 0
d = 0 serial_start = 0 serial_end = 0
d = 1 serial_start = 0 serial_end = 0
d = 0 serial_start = 0 serial_end = 0
d = 1 serial_start = 0 serial_end = 0
d = 1 serial_start = 0 serial_end = 1
Test Passed
INFO: [SIM 1] CSim done with 0 errors.
INFO: [SIM 3] *************** CSIM finish ************
```


The following figure shows the RTL/C Cosimulation output.