

1 High-Level Synthesis in FPGA, Pat 2 – Sequential Circuits

Simple Circuits-Introduction : Quiz Solution www.highlevel-synthesis.com

The divide-and-conquer technique breaks down a complex problem into two or more sub-problems. This breaking down process can recursively be applied until the subproblems become simple enough to be solved easily and independently.

Then these solutions are merged in one or several steps to find the final solution to the original problem.