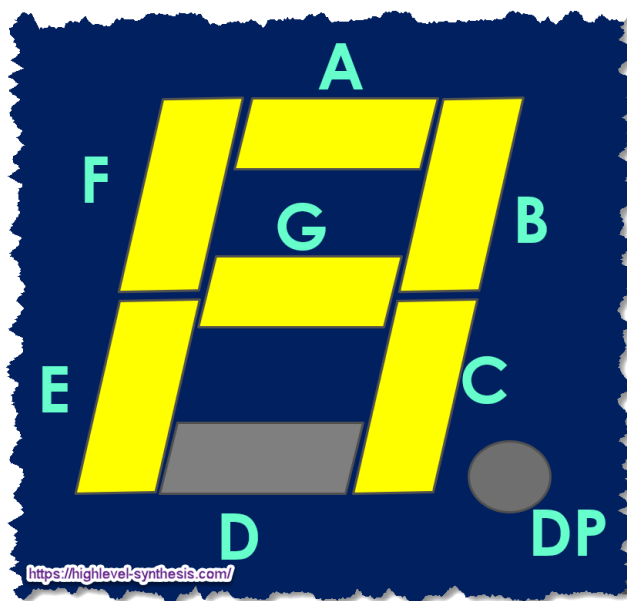


1 Digital System Design with High-Level Synthesis in FPGA

Since the 7-segment is common anode, we should drive the active segment with the '0' logic value.

Therefore, the desired code is 10001000.



DP	G	F	E	D	C	B	A
1	0	0	0	1	0	0	0